Figure 1
approved BK 02/06/04

Fig. 1

(PRIOR ART)
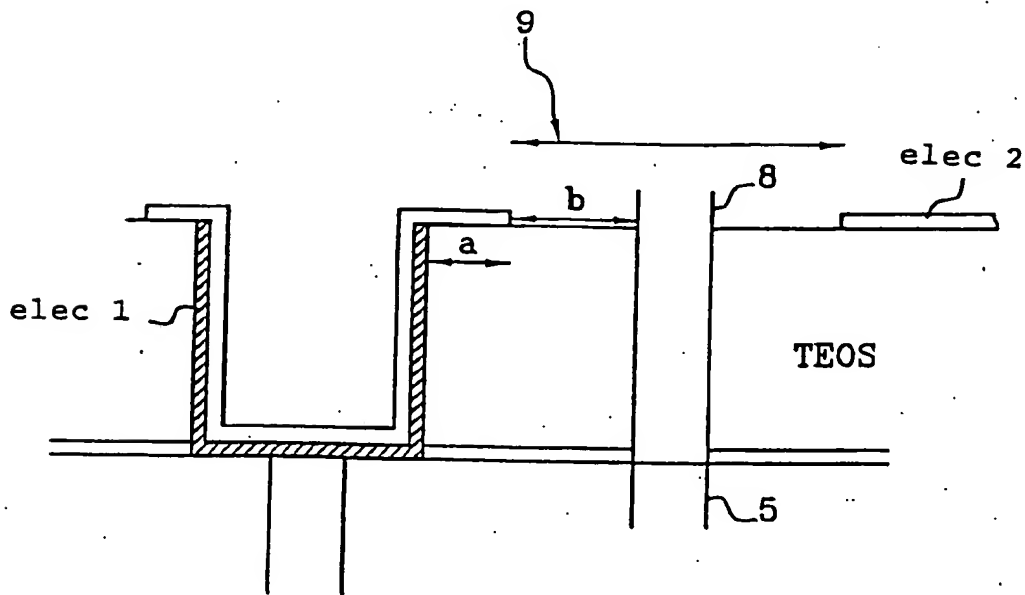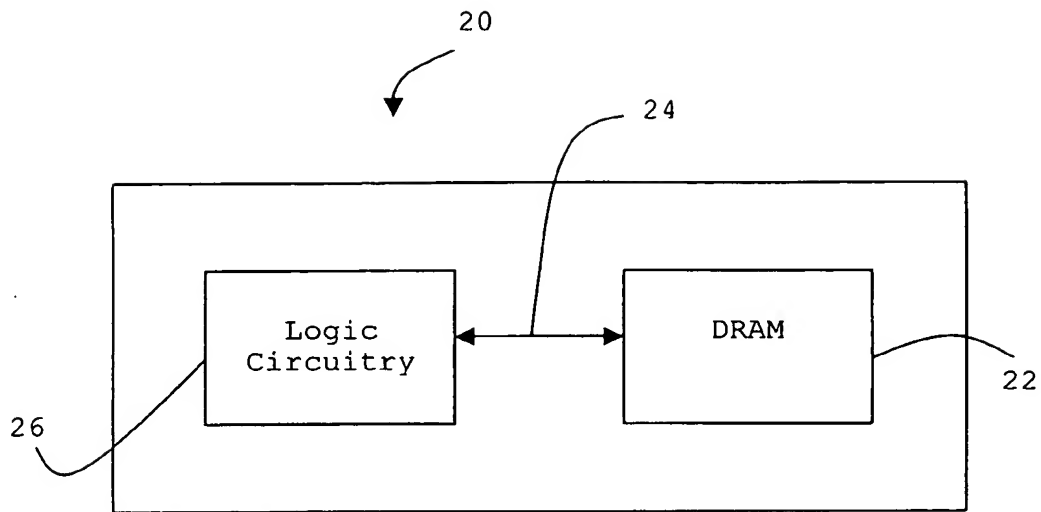

Fig. 2

(PRIOR ART)

20

24

Logic
Circuitry

DRAM

26

22

Fig. 10

Figure 10
approved
BSK 02/06/04